

## About College

Hindustan College of Science and Technology Mathura (HCST) is one of the premier institutions of Sharda Group of Institutions. HCST has eco-friendly lush Green Campus. It's campus is spread in vast area and offers sustainable green energy solutions to environmental, social and economic needs of the mankind. HCST has successfully installed DST, Govt of India supported Low Energy Membrane Desalination / Deflourination plant and Solar power plant to meet the demands of drinking water and renewable energy respectively. It's a Tech Savvy campus having different undergraduate engineering programs encompassing dedicated students and cutting edge technology trained faculties and staff.

➤ In order to cater to the needs of startups / Entrepreneurship activities in Agra and Mathura region, HCST has following Incubation centres / Hubs supported by State and Central Government Agencies

(a) iB Hubs Incubation Centre supported by Govt of UP, Lucknow

(b) Kalam Centre for Incubation supported by A.K.T.U., Lucknow

(c) MSME Incubation centre supported by Ministry of MSME, Government of India

➤ HCST has fetched National Projects of Importance in the field of Entrepreneurship

(a) PM – Yuva Yojana sponsored by Ministry of Skill Development & Entrepreneurship, Government of India

(b) DST-NIMAT project supported by EDII, Gujarat.

➤ The campus has University supported Nodal Centre for Human Values to train the students and faculty members on Professional Ethics.

➤ In order to foster the research in nanotechnology, the campus is equipped with Shri Anand Swaroop Interdisciplinary Research laboratory inaugurated by Nobel Laureate Prof. Andrew Diego Golombek.

## About the Department

The Department of Electronics and communication engineering encompasses dedicated students, Faculty and staff members. In order to foster the skill based education, the department imparts training through Govt of India sponsored schemes.

The department is equipped with well established VLSI design labs. Apart from all the university prescribed labs in the curriculum the department has a separate skill development lab to encourage students to tinker with electronic circuits.

## Key Resource Persons

- **Dr. Sushil Chandra**,  
Head, DRDO-INMAS, Delhi
- **Prof. Ram Bilas Pachori**, IIT, Indore
- **Prof. Govind Sharma**, IIT, Kanpur
- **Prof. Shankar Prakriya**, IIT, Delhi
- **Prof. Debashis Ghosh**, IIT, Roorkee
- **Dr. Ravi Babu Mulaveesala**, IIT, Ropar

## Organizing Committee

### Chief Patron(s)

**Prof. Vinay Kumar Pathak**  
VC, APJAKTU, Lucknow

**Mr. P.K. Gupta**  
Chairman, SGI

**Mr. Y.K. Gupta**  
Vice-Chairman, SGI

### Patron(s)

**Prof. V.K. Sharma**  
Exec. Director, HCST

**Dr. Rajeev Kr. Upadhyay**  
Director, HCST

### Convener

**Dr. Sanjay Jain**

Prof. & Head, Electronics & Communication Engg.

### Coordinator

**Mr. A. Mukund Lal**  
Asst. Prof., ECE  
+91-9837182266

### Co-coordinator

**Mr. T.S.S. Senthil**  
Asst. Prof., ECE  
+91-9917952671

### Programme Committee

<b>Mr. Sanjay Singh</b> ,	Asst. Prof, ECE
<b>Mrs. Rupali Mahajan</b> ,	Asst. Prof, ECE
<b>Mrs. Deepti Gupta</b> ,	Asst. Prof, ECE
<b>Mr. Ajeet Singh</b> ,	Asst. Prof, ECE
<b>Mr. Rahul Jain</b> ,	Asst. Prof, ECE
<b>Mr. Himanshu P. Rajput</b> ,	Asst. Prof, ECE
<b>Mr. Honey Kr. Arora</b> ,	Asst. Prof, ECE
<b>Mrs. Rohini Mittal</b> ,	Asst. Prof, ECE

### Administrative Assistant

**Mr. Yogesh Sharma (+91-8439760654)**



One Week

## FACULTY DEVELOPMENT PROGRAMME

on

## RESEARCH AVENUES AND TRENDS IN DIGITAL SIGNAL PROCESSING

*Computational Algorithms and Architectures*

**June 04 - 09, 2018**

## Sponsored by

**Dr. A.P.J. Abdul Kalam Technical University,  
Lucknow, Uttar Pradesh  
(Under TEQIP-III)**



## Organized by

**Department of Electronics & Communication Engineering**

**HINDUSTAN**  
COLLEGE OF SCIENCE AND TECHNOLOGY  
FARAH, MATHURA

(Affiliated to Dr. A.P.J. Abdul Kalam Technical University,  
Lucknow, Uttar Pradesh)

**Campus: 26<sup>th</sup> Km Milestone Agra - Delhi Highway,  
Farah, Mathura - 281122 (UP)**

**Website: www.hcst.edu.in**



## About FDP

This faculty development program is expected to bring different learned persons in the field of Digital signal processing, researchers and persons from industry on to a common platform for exchange of knowledge and ideas in the field .

This can help faculty innovate and be more effective. It also helps share scholarly findings in the areas research with the participants. Participants will explore recent advances in academic research, pedagogies, research-based instructional practices and techniques.

The knowledge of state of art technologies in the area of digital signal processing algorithms and architectures would percolate into the class room teaching and increase the quality of teaching- learning process.

## Major topics to be covered

- Digital signal Processing algorithms
- Statistical Signal Processing
- Speech and Acoustic Signal Processing
- Biomedical Signal Processing
- Signal Processing for Communications
- Signal Processing Architectures
- Analog and Digital signal Processing
- Adaptive Signal Processing
- Signal detection and analysis

## Who can apply?

All the faculty members from Under Graduate / Post Graduate programmes of AICTE approved technical institutions and affiliated to Dr. A.P.J. Abdul Kalam Technical University, Lucknow

## Registration

The participants are requested to register their names by sending the duly filled registration form in the format attached herewith. Send the scanned copy of filled registration form to email given below on or before 30<sup>th</sup> May 2018.

Email ID: sanjay.jain.hcst@sgei.org

## Registration Fee & Certificate

No registration fee for attending the Faculty Development Programme. Participation certificate will be provided to all the participants by the institute.

## Travel & Accomodation

Participants will have to make their own arrangements for their travel. Fooding and Lodging facility for outside participants will be arranged in campus on first come first serve basis as per capacity.

## Contact for Correspondence

### Mr. A. Mukund Lal

Assistant Professor  
Department of Electronics & Communication  
Hindustan College of Science and Technology,  
26th Km Milestone, Agra-Delhi Highway,  
Farah, Mathura-281122 (U.P.), India

Contact No: +91-9837182266

Email ID: sanjay.jain.hcst@sgei.org

Website: <http://www.hcst.edu.in>



## Registration Form FACULTY DEVELOPMENT PROGRAMME on

## RESEARCH AVENUES AND TRENDS IN DIGITAL SIGNAL PROCESSING

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Lucknow, Uttar Pradesh  
(Under TEQIP-III)

1. NAME: \_\_\_\_\_

(in CAPITAL LETTERS)

2. Designation: \_\_\_\_\_

3. Organization: \_\_\_\_\_

4. Correspondence Address: \_\_\_\_\_

5. Contact No: \_\_\_\_\_

6. Email ID: \_\_\_\_\_

Date: \_\_/\_\_/\_\_

Signature (Applicant)

-Bonafide Certificate-

Prof./Dr./Ms./Mr. \_\_\_\_\_  
is a bonafide faculty of our college. He/She is permitted  
to attend the Faculty Development Programme on  
"Research Avenues in DSP" during 04-09 June, 2018.

Date: \_\_/\_\_/\_\_

Signature of Director  
with seal